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Docket No.: 060188-0075

MAY 15 2006**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
FUJIWARA, MAKOTO	:	Confirmation Number: 5700
Application No.: 09/867,766	:	Group Art Unit: 2134
Filed: May 31, 2001	:	Examiner: Poltorak, Piotr
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING SEMICONDUCTOR INTEGRATED CIRCUIT		

REQUEST FOR RECONSIDERATION

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated January 13, 2006, having a three-month shortened statutory period for response set to expire on April 13, 2006, and the petition for a one-month extension of time up to and including May 13, 2006 (Saturday) being filed concurrently herewith, reconsideration of the above-identified application is respectfully requested in view of the following remarks.

WDC99 1234212-1.060188.0075